



SAKSHAR GRAMIN VIKAS SEVA SANSTHA'S
ADSUL'S TECHNICAL CAMPUS

At/Post - Chas, Chas Nimbalek Bypass Road Tal - Nagar, Dist - Ahmednagar
(Approved By AE New Delhi, Recognized by Government of Maharashtra, DTZ and Affiliated to SPPU, Pune)

Electrical Engineering Department									
SEM-II									
WITH EFFECT FROM 01-01-2025									
ROOM NO: 31.32.33									
PERIODS									
Timing	Class	I	II	III	IV	V	VI		
		10.00 To 11.00	11.00 To 12.00	12.30 To 1.30	1.30 To 2.30	2.45 To 3.45	3.45 To 4.45		
MONDAY	SE	PS-I (DR. SK)	EM-I (HRS)	NMCP (MSK)	NT (JVM)	CSE/CADEM/PS-II / IIBRARY T1 / T2 / T3 / T4 RBY/PBS/PDL	EM/NMCP S1 / S2 HRS/MSK		
	TE	PS-II (DR. DJ)	CADEM (DR. NSB)	AC-IV (ARS)	CSE (RBY)				
	BE	AEDC (DR. MBK)	SG (ELEC-III) (HKB)	AC-VIII (PSN)	IL (ELEC-IV) (PSK)				
TUESDAY	SE	PS-I (DR. SK)	EM-I (HRS)	NT (JVM)	Library	CSE/CADEM/PS-II / IIBRARY T3 / T4 / T1 / T2 RBY/PBS/PDL			
	TE	PS-II (DR. DJ)	CADEM (DR. NSB)	CSE (RBY)	PS-II (TU) (DR. DJ)	CSE/CADEM/PS-II / IIBRARY T4 / T1 / T2 / T3 RBY/PBS/PDL			
	BE	SG (ELEC-III) (HKB)	AEDC (DR. MBK)	IL (ELEC-IV) (PSK)	SGP (PDL)	SGP/AEDC/LIBRARY/LIBRARY B2 / B1 / B3 / B4 SAE/DI			
WEDNESDAY	SE	PS-I (DR. SK)	EM-I (HRS)	NT (JVM)	FMA (RS)	FMA/PBL S1/S2 DI/RS			
	TE	EM(ELEC-II) (RS)	PS-II (DR. DJ)	CSE (RBY)	CSE (TU) (RBY)	CSE/CADEM/PS-II / IIBRARY T3 / T4 / T1 / T2 RBY/PBS/PDL			
	BE	AEDC (DR. MBK)	SG (ELEC-III) (HKB)	SGP (PDL)	EXTRA CLASS/DOUBT CLASS	CSE/CADEM/PS-II / IIBRARY T2 / T3 / T4 / T1 RBY/PBS/PDL			
THURSDAY	SE	NMCP (MSK)	FMA (RS)	Project learning		FMA/PBL S2/S1 DI/RS			
	TE	CADEM (DR. NSB)	EM(ELEC-II) (RS)	LIBRARY		CSE/CADEM/PS-II / IIBRARY T2 / T3 / T4 / T1 RBY/PBS/PDL			
	BE	AC-VIII (PSN)	IL (ELEC-IV) (PSK)	SGP (PDL)	EXTRA CLASS/DOUBT CLASS	SGP/AEDC/LIBRARY/LIBRARY B4/ B3 /B2 /B1 SAE/DI			
FRIDAY	SE	NMCP (MSK)	FMA (RS)	Project learning		LIBRARY			
	TE	EM(ELEC-II) (RS)	AC-IV (ARS)	LIBRARY		SPORTS			
	BE	PROJECT-I		PROJECT-I		PROJECT-I			

Time Table Coordinator

HOD

Head
Electrical Engg. Dept. 4th Nagar

Central Time Table Coordinator

Principal
Dr. Pradeep M. Patil
Principal & Director